TD n°2 : Gestion de la mémoire

On considère quatre tables de segments

Segment	0	1	2	3	4
Base	540	1254	54	2048	976
longueur	234	128	325	1024	200

1) Calculer les adresses réelles des adresses virtuelles suivantes

(0, 128) → 540 + 128

(1, 100) → 1254 + 100

(3, 888) → 2048 + 888

(4, 344) → erreur

(1, 99) → 1254 + 99

(2, 495) → erreur

(4, 100) → 976 + 100

2) Page de 2000 octets pour simplifier le calcul (2048 normalement)

a) 10541 → page 5 → erreur de page (bit de présence 0)

5421 → page 2

14123 → page 7

9156 → page 4 → erreur de page

b) adresses physiques :

5421 → (200 × 2000) + (5421 − 2 × 2000)

14123 → (101 × 2000) + (14123 − 7 × 2000)

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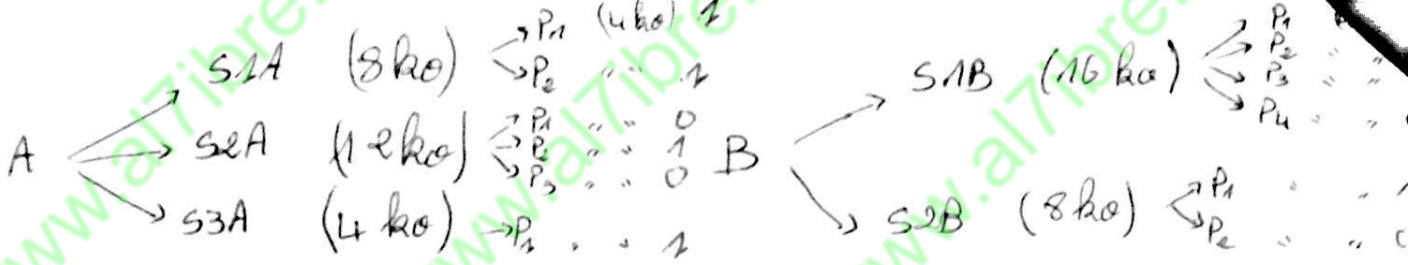
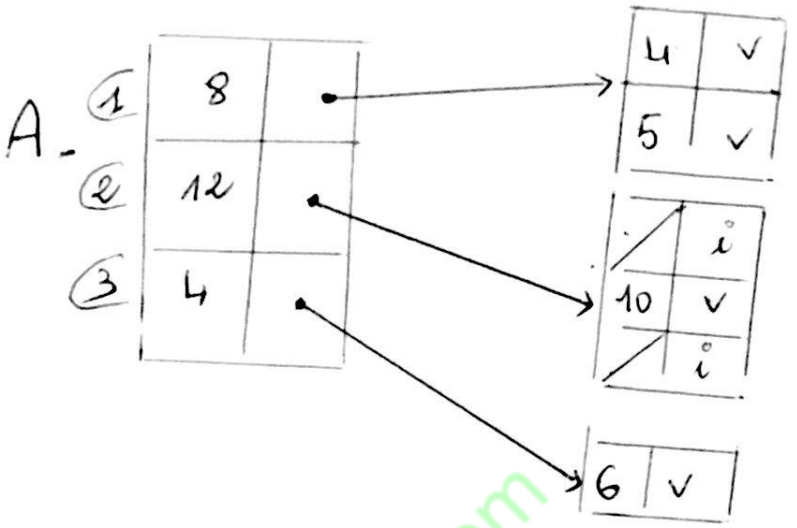
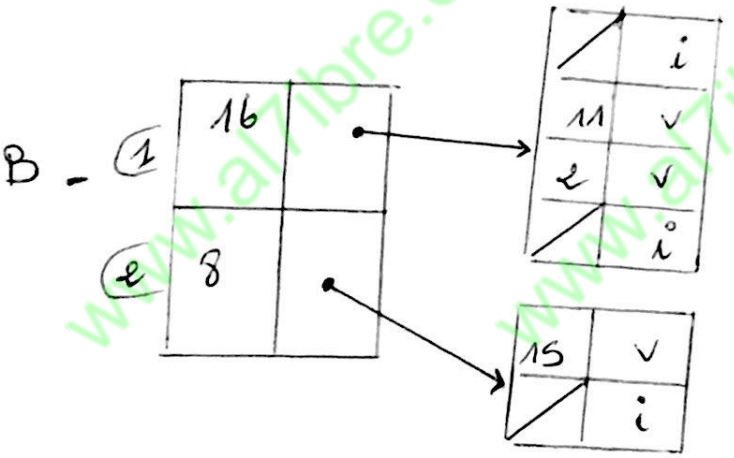
3/



La mémoire centrale est divisée en 15 cases de 1 à 15 [(case = cache) 4 ko (4 ko = 4096 o)



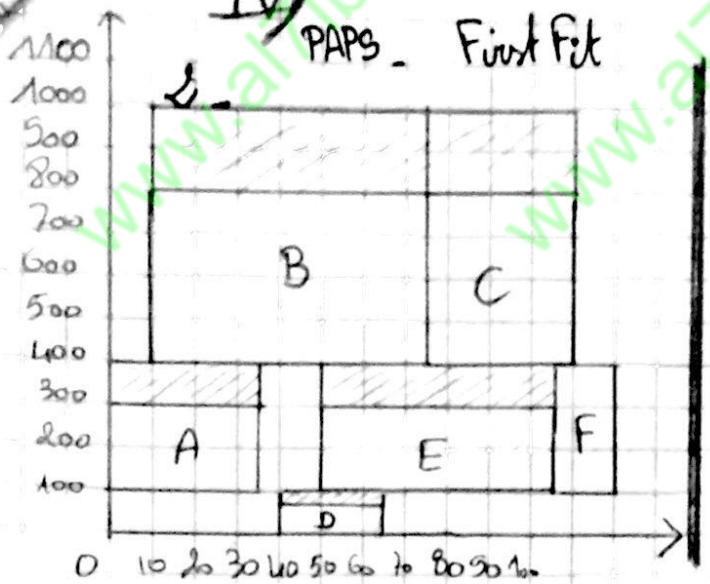


4/ Donner pour chacune des adresses relatives son équivalent en adresse virtuelle, puis son adresse physique :

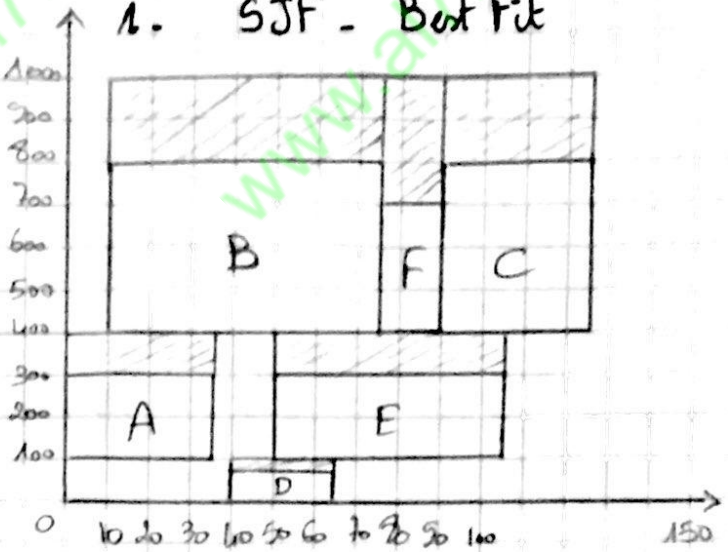
		Seg	Pg	dec		Cache dec
A	4098	1	2	2	→	(5, 2)
	12292	2	2	4	→	(10, 4)
B	8212	1	3	20	→	(2, 20)

IV)

2- PAPS - First Fit

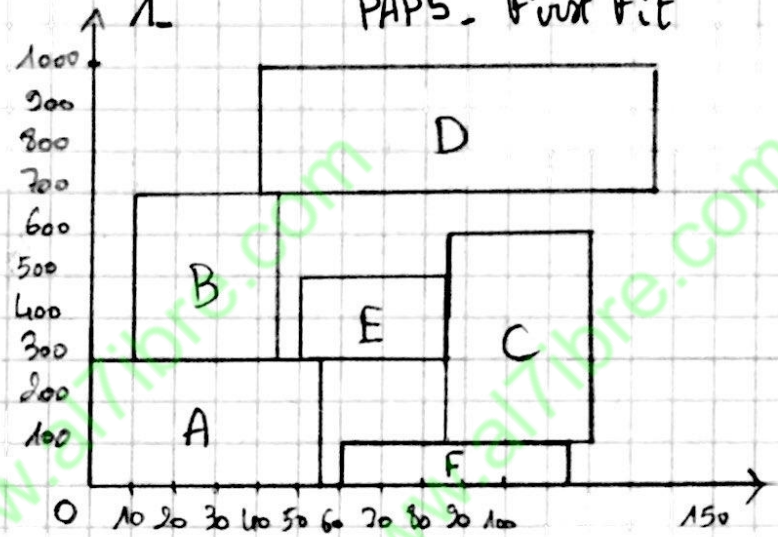


1- SJF - Best Fit

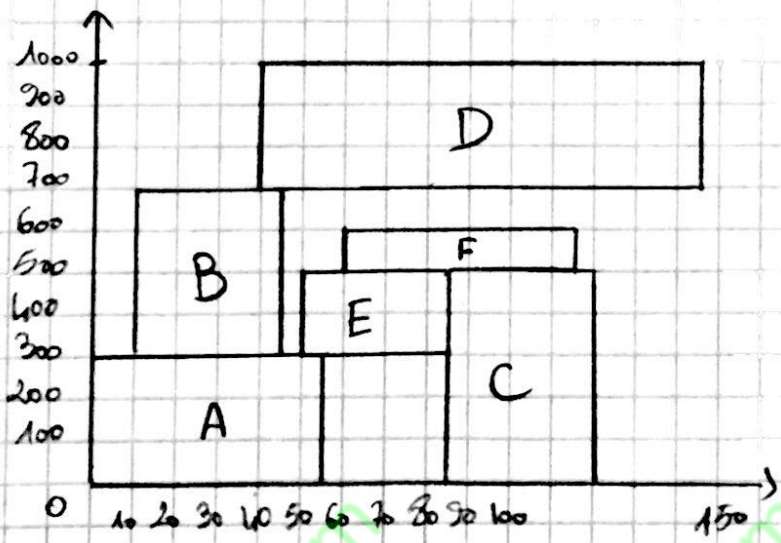


V)

1- PAPS - First Fit



2- PAPS - Best Fit



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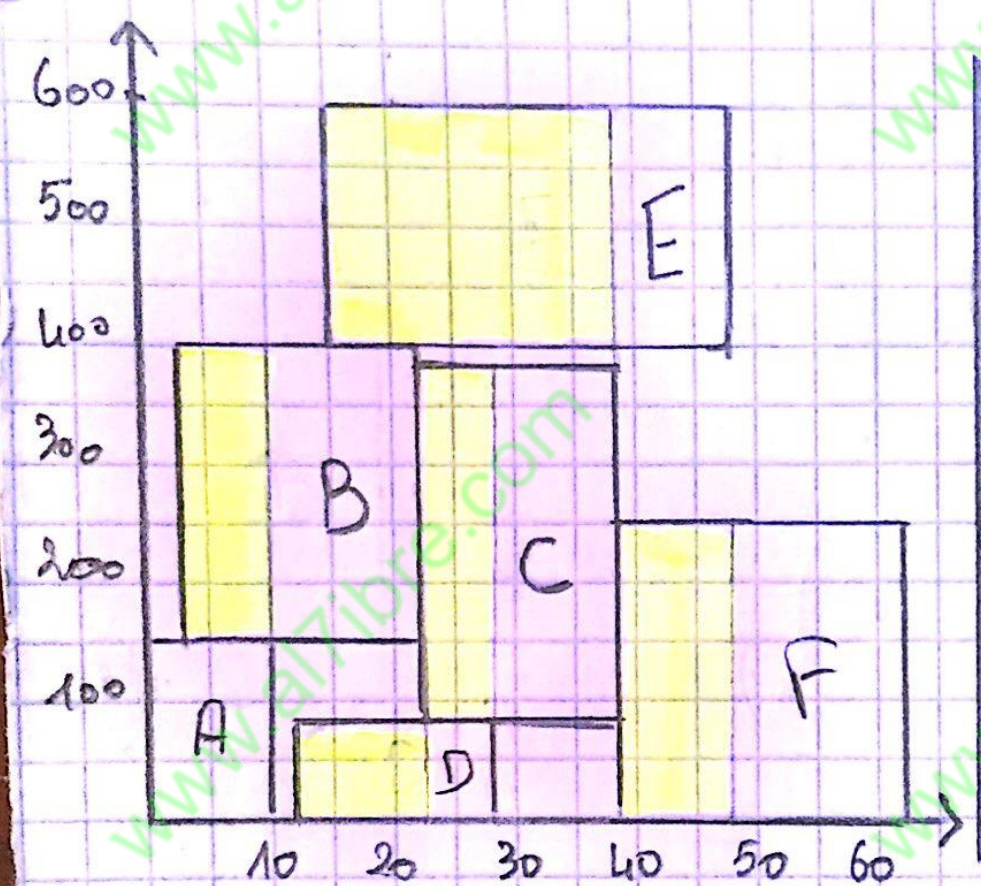
VI)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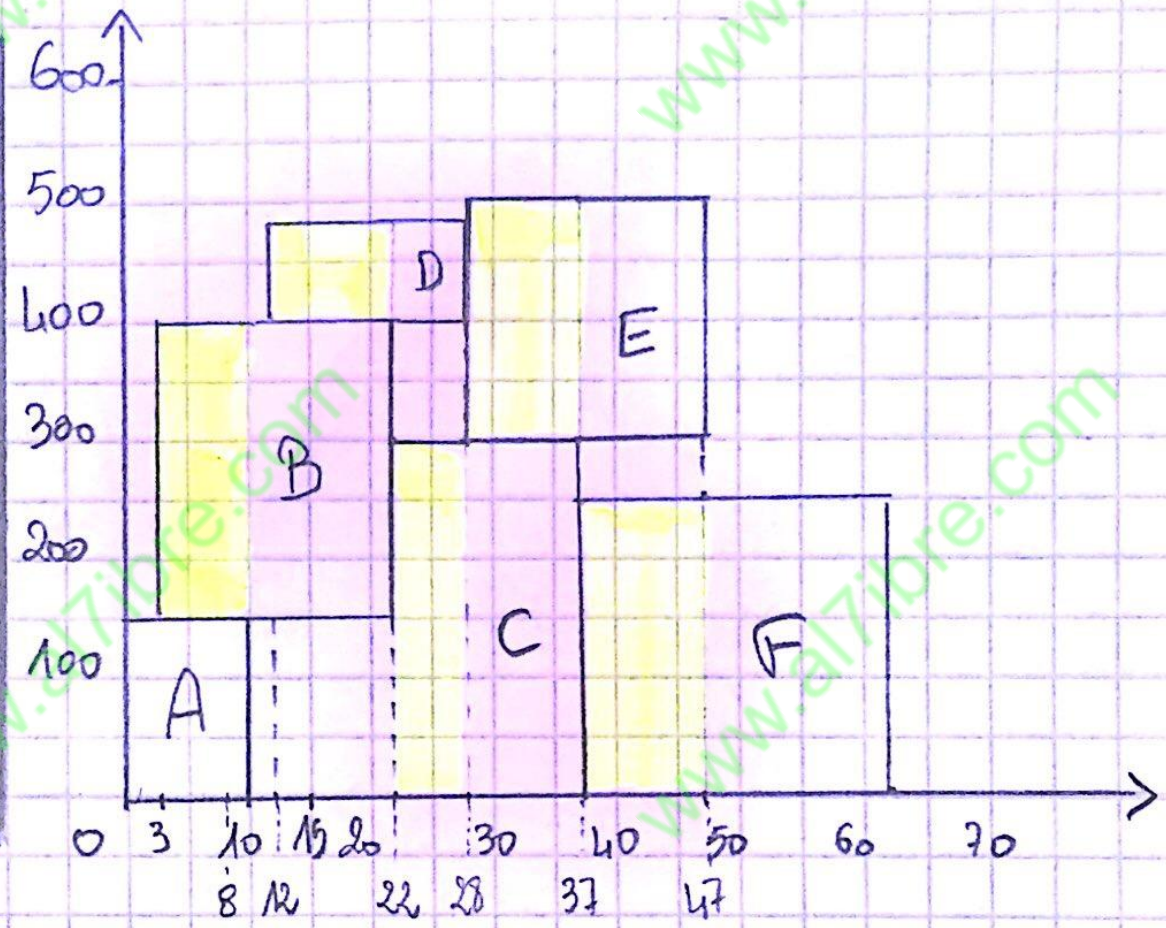
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VII)